

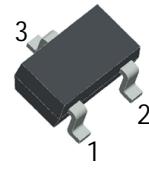


Foshan city Hexin semiconductor Co., Ltd.

## HS2302 N-Channel 20-V(D-S) MOSFET

| $V_{(BR)DSS}$ | $R_{DS(on)}\text{MAX}$ | $I_D$ |
|---------------|------------------------|-------|
| 20V           | 22 mΩ@4.5V             | 3.8A  |
|               | 33 mΩ@2.5V             |       |

SOT-23



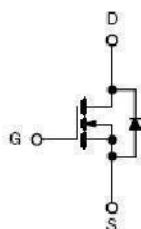
### FEATURE

- TrenchFET Power MOSFET

### APPLICATION

- Load Switch for Portable Devices
- DC/DC Converter

### Equivalent Circuit



### Maximum ratings ( $T_a=25^\circ\text{C}$ unless otherwise noted)

| Parameter  | Symbol          | Value     | Unit                      |
|--|-----------------|-----------|---------------------------|
| Drain-Source Voltage   | $V_{DS}$        | 20        | V                         |
| Gate-Source Voltage  | $V_{GS}$        | $\pm 8$   |                           |
| Continuous Drain Current   | $I_D$           | 4         | A                         |
| Continuous Source-Drain Current(Diode Conduction)                  | $I_S$           | 0.6       |                           |
| Power Dissipation  | $P_D$           | 0.4       | W                         |
| Thermal Resistance from Junction to Ambient ( $t \leq 5\text{s}$ ) | $R_{\theta JA}$ | 312.5     | $^\circ\text{C}/\text{W}$ |
| Operating Junction   | $T_J$           | 150       | $^\circ\text{C}$          |
| Storage Temperature  | $T_{STG}$       | -55 ~+150 |                           |

## MOSFET ELECTRICAL CHARACTERISTICS

T<sub>a</sub>=25 °C unless otherwise specified

| Parameter                                 | Symbol               | Test Condition  | Min  | Typ   | Max   | Units |
|---|----------------------|---|------|-------|-------|-------|
| <b>Static</b>                             |                      |   |      |       |       |       |
| Drain-source breakdown voltage            | V <sub>(BR)DSS</sub> | V <sub>GS</sub> = 0V, I <sub>D</sub> = 10µA   | 20   |       |       | V     |
| Gate-threshold voltage                    | V <sub>GS(th)</sub>  | V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 50µA   | 0.65 | 0.95  | 1.2   |       |
| Gate-body leakage                         | I <sub>GSS</sub>     | V <sub>DS</sub> = 0V, V <sub>GS</sub> = ±8V   |      |       | ±100  | nA    |
| Zero gate voltage drain current           | I <sub>DSS</sub>     | V <sub>DS</sub> = 20V, V <sub>GS</sub> = 0V   |      |       | 1     | µA    |
| Drain-source on-resistance <sup>a</sup>   | R <sub>DSS(on)</sub> | V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 2A   |      | 0.022 | 0.045 | Ω     |
|   |                      | V <sub>GS</sub> = 2.5V, I <sub>D</sub> = 2A   |      | 0.033 | 0.06  |       |
| Forward transconductance <sup>a</sup>     | g <sub>fs</sub>      | V <sub>DS</sub> = 5V, I <sub>D</sub> = 3.6A   |      | 8     |       | S     |
| Diode forward voltage                     | V <sub>SD</sub>      | I <sub>S</sub> = 0.94A, V <sub>GS</sub> = 0V  |      | 0.76  | 1.2   | V     |
| <b>Dynamic</b>                            |                      |   |      |       |       |       |
| Total gate charge                         | Q <sub>g</sub>       | V <sub>DS</sub> = 10V, V <sub>GS</sub> = 4.5V, I <sub>D</sub> = 2A  |      | 4.0   | 10    | nC    |
| Gate-source charge                        | Q <sub>gs</sub>      |   |      | 0.65  |       |       |
| Gate-drain charge                         | Q <sub>gd</sub>      |   |      | 1.5   |       |       |
| Input capacitance <sup>b</sup>            | C <sub>iss</sub>     | V <sub>DS</sub> = 10V, V <sub>GS</sub> = 0V, f = 1MHz   |      | 300   |       | pF    |
| Output capacitance <sup>b</sup>           | C <sub>oss</sub>     |   |      | 120   |       |       |
| Reverse transfer capacitance <sup>b</sup> | C <sub>rss</sub>     |   |      | 80    |       |       |
| <b>Switching<sup>b</sup></b>              |                      |   |      |       |       |       |
| Turn-on delay time                        | t <sub>d(on)</sub>   | V <sub>DD</sub> = 10V,<br>R <sub>L</sub> = 5.5Ω, I <sub>D</sub> ≈ 3.6A,<br>V <sub>GEN</sub> = 4.5V, R <sub>g</sub> = 6Ω |      | 7     | 15    | ns    |
| Rise time                                 | t <sub>r</sub>       |   |      | 55    | 80    |       |
| Turn-off delay time                       | t <sub>d(off)</sub>  |   |      | 16    | 60    |       |
| Fall time                                 | t <sub>f</sub>       |   |      | 10    | 25    |       |

### Notes :

a. Pulse Test : Pulse width ≤ 300µs, duty cycle ≤ 2%.

b. These parameters have no way to verify.

## Typical Characteristics

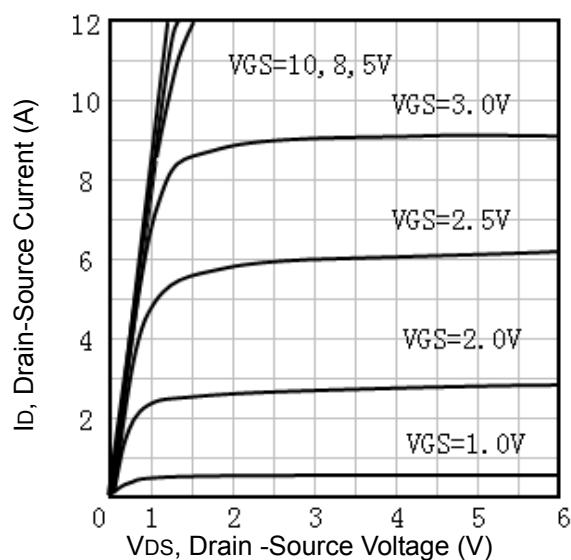


Fig1. Typical Output Characteristics

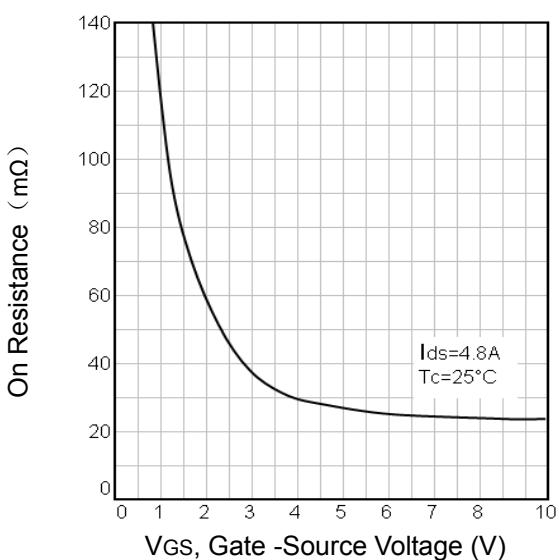


Fig2. Typical Transfer Characteristics

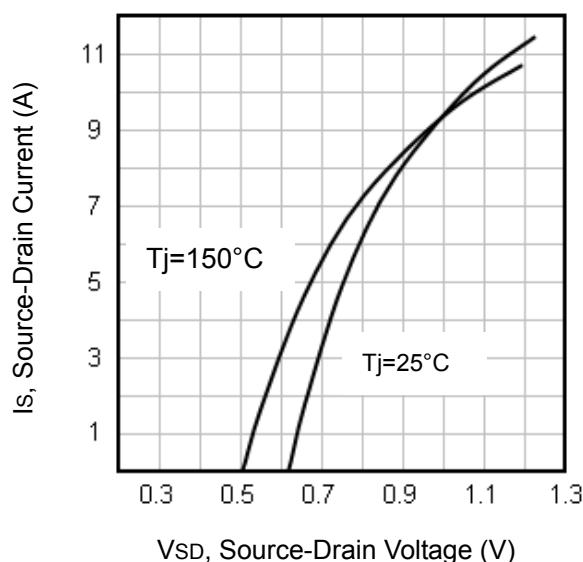


Fig7. Typical Source-Drain Diode Forward Voltage

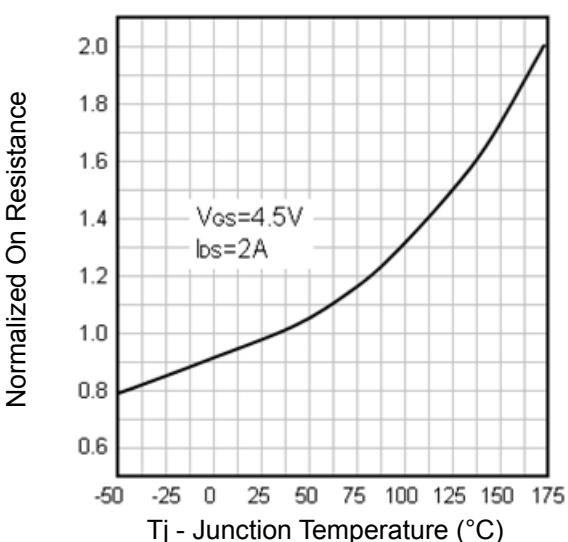


Fig4. Normalized On-Resistance Vs. Temperature

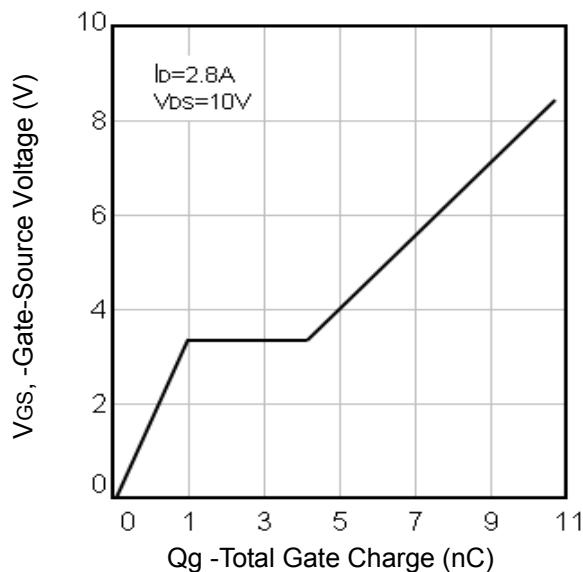


Fig5. Typical Gate Charge Vs. Gate-Source Voltage

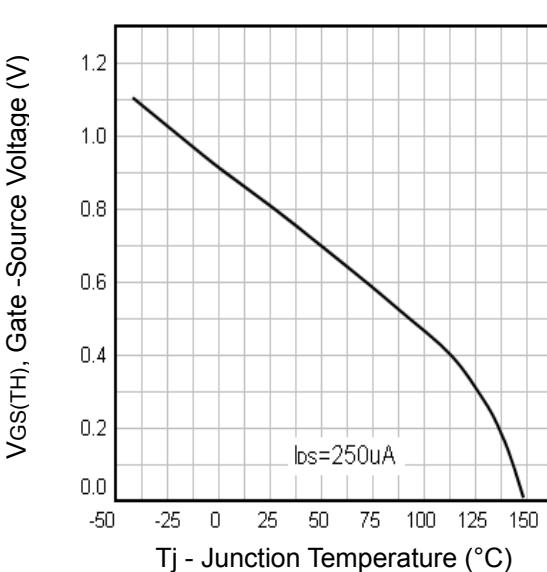


Fig6. Threshold Voltage Vs. Temperature